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Vishay Semiconductors

AUTOMOTIVE

HALOGEN

FREE GREEN

High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

As part of the <u>SurfLight</u>TM portfolio, the VSMY2941X01 series are infrared, 940 nm emitting diodes based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

FEATURES

Package type: surface-mountPackage form: GW, RGW

• Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8

Peak wavelength: λ_p = 940 nm

AEC-Q101 qualified

High radiant power

• Very high radiant intensity

• Angle of half intensity: $\varphi = \pm 8^{\circ}$

Terminal configurations: gullwing or reverse gullwing

Package matches with detector VEMD2000X01 series

Floor life: 4 weeks, MSL 2a, according to J-STD-020

 Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>



- · Miniature light barrier
- Photointerrupters
- · Optical switch
- Emitter source for proximity sensors
- Head-up displays

PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (°)	$\lambda_{\mathbf{P}}$ (nm)	t _r (ns)	
VSMY2941RGX01	160	± 8	940	5	
VSMY2941GX01	160	± 8	940	5	

Note

· Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY2941RGX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing		
VSMY2941GX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing		

Note

• MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	5	V
Forward current		I _F	70	mA
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	140	mA
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA
Power dissipation		P_V	120	mW
Junction temperature		T _j	100	°C
Operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Soldering temperature	According to Fig. 10, J-STD-020	T _{sd}	260	°C
Thermal resistance junction to ambient	J-STD-051, soldered on PCB	R _{thJA}	250	K/W

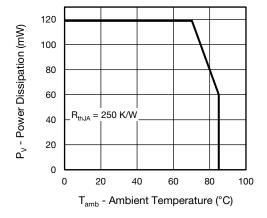


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 50 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.4	1.7	V
Forward voltage	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.5	_	V
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	V_{F}	-	2.6	-	V
Temperature coefficient of V _F	I _F = 50 mA	TK _{VF}	-	-0.7	-	mV/K
Reverse current		I _R	Not designed for reverse operation		e operation	μΑ
Junction capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}, E = 0 \text{ mW/cm}^2$	CJ	-	30	-	рF
	$I_F = 50 \text{ mA}, t_p = 20 \text{ ms}$	l _e	60	115	170	mW/sr
Radiant intensity	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	l _e	-	160	_	mW/sr
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	l _e	-	850	-	mW/sr
Radiant power	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	фe	-	40	-	mW
Temperature coefficient of radiant power	I _F = 50 mA	TKφ _e	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 8	-	0
Peak wavelength	I _F = 50 mA	λ_{p}	920	940	960	nm
Spectral bandwidth	I _F = 70 mA	Δλ	-	55	_	nm
Temperature coefficient of λ _p	I _F = 70 mA	$TK\lambda_p$	-	0.28	-	nm/K
Rise time	I _F = 70 mA, 10 % to 90 %	t _r	-	5	-	ns
Fall time	I _F = 70 mA, 10 % to 90 %	t _f	-	6	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

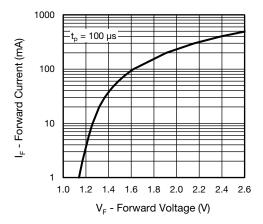


Fig. 3 - Forward Current vs. Forward Voltage

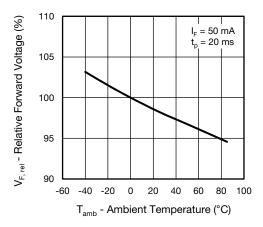


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

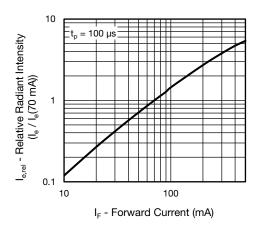


Fig. 5 - Radiant Intensity vs. Forward Current

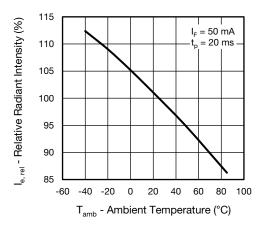


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

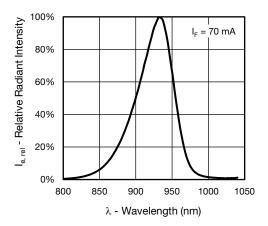


Fig. 7 - Relative Radiant Intensity vs. Wavelength

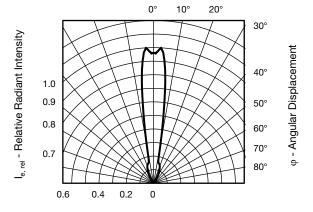


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

SOLDER PROFILE

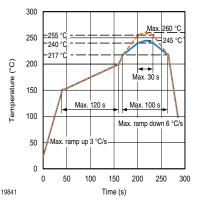


Fig. 9 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

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DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

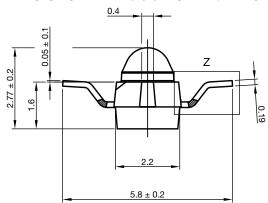
FLOOR LIFE

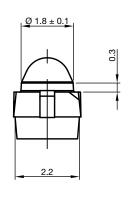
Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions: T_{amb} < 30 °C, RH < 60 % Moisture sensitivity level 2a, according to J-STD-020.

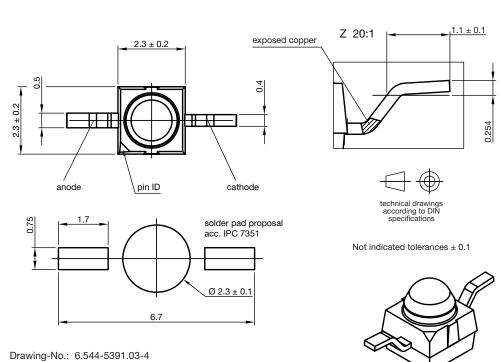
DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

PACKAGE DIMENSIONS in millimeters: VSMY2941RGX01

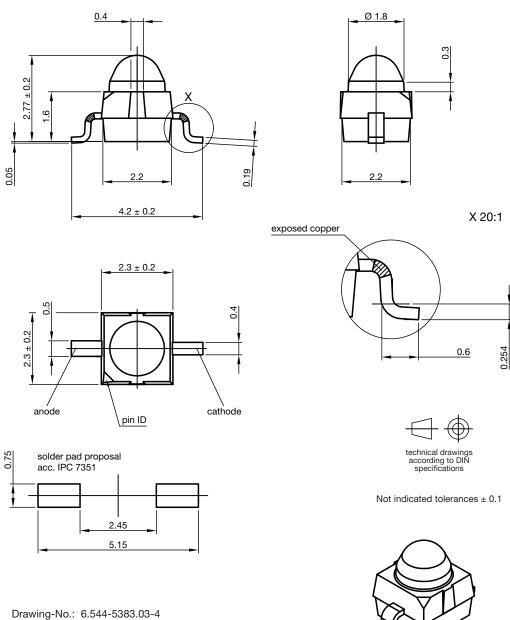






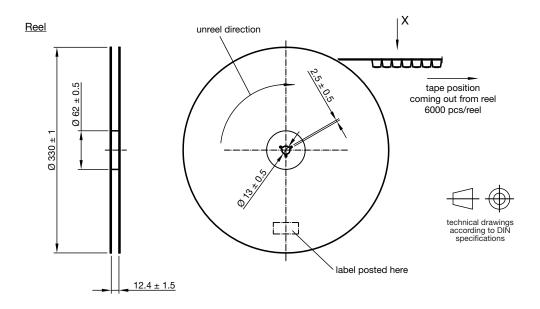
Rev. 1.1, 12-Mar-2025 **4** Document Number: 84573

PACKAGE DIMENSIONS in millimeters: VSMY2941GX01

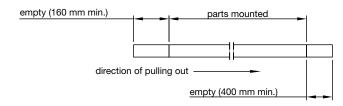


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TAPING AND REEL DIMENSIONS in millimeters: VSMY2941RGX01



Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II	
VEMT2000	Collector	Fmitter	
VEMT2500	Collector	Emiller	
VEMD2000			
VEMD2500			
VSMB2000	Cathode	Anode	
VSMG2000			
VSMF2890RG			
VSMY2850RG	Anode	Cathode	
VSMY2940RG	Alloue	Califode	

 $.75 \pm 0.1$ 0.3 12± 5.5 ± 0.05 3.05 ± 0.1 II

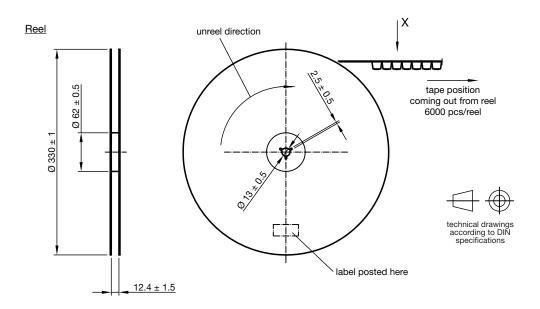
 4 ± 0.1

Ø 1.55 ± 0.05

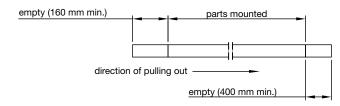
Drawing-No.: 9.800-5100.01-4

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TAPING AND REEL DIMENSIONS in millimeters: VSMY2941GX01

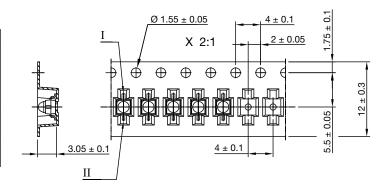


Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II	
VSMB2020		Anode	
VSMG2020			
VEMD2020	Cathode		
VEMD2520			
VSMF2890G			
VEMT2020	Collector	Emitter	
VEMT2520	Collector	Emiller	
VSMY2850G	Anode	Cathode	
VSMY2940G	Anoue	Califode	



Drawing-No.: 9.800-5091.01-4

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