RoHS

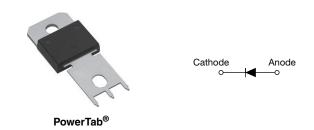
**HALOGEN** 

FREE



Vishay Semiconductors

# Ultrafast Soft Recovery Diode, 80 A FRED Pt®



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	80 A			
$V_{R}$	200 V			
V <sub>F</sub> at I <sub>F</sub>	0.79 V			
t <sub>rr</sub> (typ.)	See recovery table			
T <sub>J</sub> max.	175 °C			
Package	PowerTab <sup>®</sup>			
Circuit configuration	Single			

#### **FEATURES**

- · Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- AEC-Q101 qualified
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **BENEFITS**

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- Reduced parts count

### **DESCRIPTION / APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

#### **MECHANICAL DATA**

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating

Terminal: nickel plated screwable

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	$V_R$		200	V
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 112 °C	80	
Single pulse forward current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	800	Α
Maximum repetitive forward current	I <sub>FRM</sub>	Square wave, 20 kHz	160	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MAX		MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>r</sub>	I <sub>R</sub> = 50 μA	200	-	-	V
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 80 A	-	0.94	1.13	V
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 80 A, T <sub>J</sub> = 175 °C	-	0.79	0.92		
Reverse leakage current	1	$V_R = V_R$ rated	-	-	50	μΑ
neverse leakage current	I <sub>R</sub>	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	-	2	mA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	89	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	3.5	-	nΗ



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt = 200 A		A/μs, V <sub>R</sub> = 30 V	-	-	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	32	-	ns
		T <sub>J</sub> = 125 °C		-	52	-	
Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 80 \text{ A}$ $V_R = 160 \text{ V}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	4.4	-	Α	
	T <sub>J</sub> = 125 °C		-	8.8	-		
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C	· '	-	70	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	240	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.70	· °C/W
Thermal resistance, junction to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	C/VV
Weight			-	-	5.02	g
Mounting torque			1.2 (10)	-	2.4 (20)	N · m (lbf · in)
Marking device		Case style PowerTab®		80E	3U02	

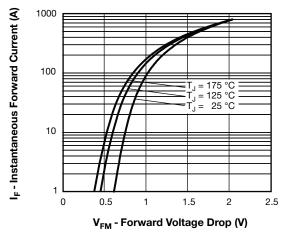


Fig. 1 - Maximum Forward Voltage Drop Characteristics

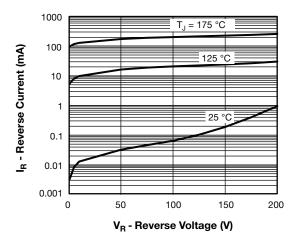


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

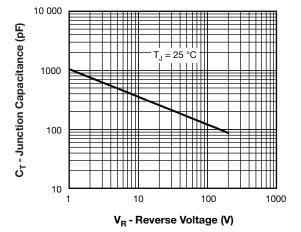


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

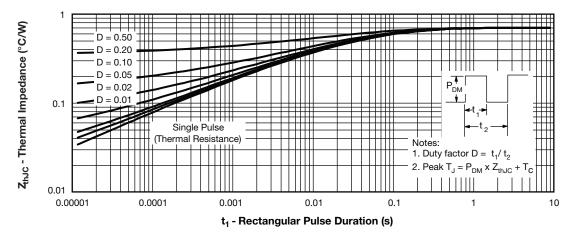


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

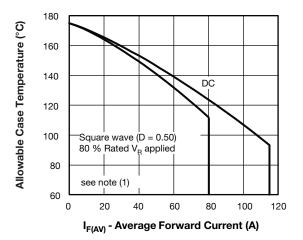


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

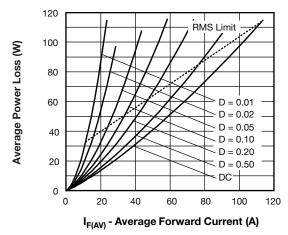


Fig. 6 - Forward Power Loss Characteristics

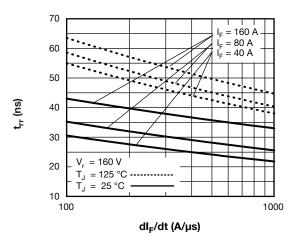


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_{\text{F}}/dt$ 

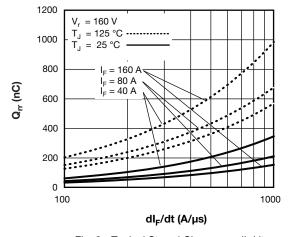
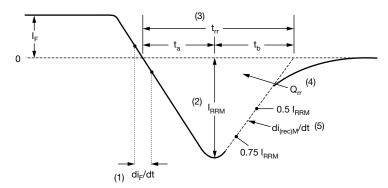


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$ 

### Note

(1) Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\rm Q_{rr}$  area under curve defined by  $\rm t_{rr}$  and  $\rm I_{RRM}$

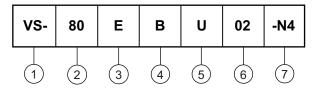
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (80 = 80 A)
- 3 Single diode
- PowerTab<sup>®</sup> (ultrafast / hyperfast only)
- 5 Ultrafast recovery
- 6 Voltage rating (02 = 200 V)
- 7 Environmental digit:

-N4 = Halogen-free, RoHS-compliant and totally lead (Pb)-free

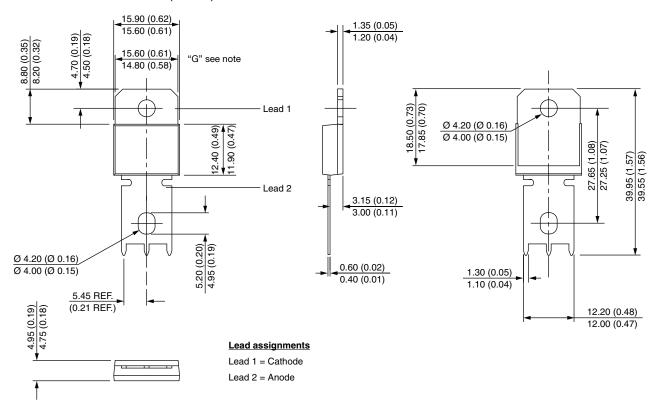
ORDERING INFORMATION (Example)				
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION				
VS-80EBU02-N4	25/tube	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			



### PowerTab®

### **DIMENSIONS** in millimeters (inches)



#### Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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Vishay

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